	REVISIONS		
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
E	Change to military drawing format. Table I, change V _{CM} min to -0.3 V. Add vendors 48726, U4637, and 34333. Editorial changes throughout.	1987 APR 21	M. A. Frye
F	Add parameters to absolute maximum ratings. Page 4, reference section: Change I _{OS} max limit from 100 mA to 150 mA: change ripple rejection from max of 66 dB to min of 60 dB guaranteed by correlation. Page 4, oscillator section: Change temperature stability max limit from 2 percent to 6 percent guaranteed. Page 5, output section: Add tests for rise time and fall time. Editorial changes throughout.	1987 DEC 1	M. A. Frye
G	Add vendor CAGE 18324. Change name for vendor CAGE U4637. Editorial changes throughout. Make drawing inactive for new design.	1988 DEC 8	M. A. Frye

Inactive for new design as of 8 DEC 1988. Use QPL M38510/12601BEX

REV

CURRENT CAGE CODE 67268

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THIS DRAWING IS AVAILABLE FOR USE BY ALL APPROVED BY Michael A. Frye WIDTH MODULATOR,															
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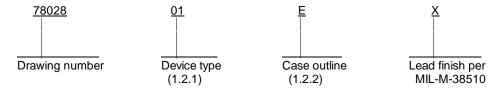
1 OF 10

AMSC N/A

G

1. SCOPE

- 1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".
 - 1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 <u>Device type</u>. The device type shall identify the circuit function as follows:

Device type	Generic number	<u>Circuit function</u>
01	1524	Regulating pulse width modulator

1.2.2 Case outline. The case outline shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter Case outline Ε D-2 (16-lead, .840" x .310" x .200"), dual-in-line package

1.3 Absolute maximum ratings.

Input voltage (V _{IN})	40 V dc
Collector output current	100 mA dc
Reference output current	50 mA dc
Power dissipation at T _A = +25°C <u>1</u> /	1,000 mW
Power dissipation at T_C° = +25°C $\underline{2}$ /	2,000 mW
Junction temperature (T _J)	+150°C
Thermal resistance:	
Junction-to-ambient (O _{JA})	100° C/W
Junction-to-case (O _{.I.C})	(See MIL-M-38510, appendix C)
Oscillator charging current	5 mA
Storage temperature range	-65° C to +150° C
Lead temperature (soldering, 10 seconds)	+300°C

1.4 Recommended operating conditions.

Ambient operating temperature range (T _A)	-55° C to +125° C
Input voltage range	8 V dc to 40 V dc
Current through C _T terminal	-0.03 mA to -2 mA
Timing resistor (R _T)	1.8 k Ω to 100 k Ω
Timing capacitor (C _T)	0.001 μF to 0.1 μF

^{1/} Derate at 10 mW/° C for T_A above +50° C. 2/ Derate at 16 mW/° C for T_C above +25° C.

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2. APPLICABLE DOCUMENTS

2.1 <u>Government specification and standard</u>. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510

- Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883

- Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 <u>Item requirements</u>. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Logic diagram. The logic diagram shall be as specified on figure 2.
 - 3.2.3 <u>Case outline</u>. The case outline shall be in accordance with 1.2.2 herein.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.
- 3.4 <u>Marking</u>. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Tes	st	Symbol	Conditions 1/	Group A	Limit	Unit	
			-55° C ≤ T _A ≤ +125° C unless otherwise specified	sub- groups	Min	Max	
Reference section	Output voltage	V _{REF}		1,2,3	4.8	5.2	V
	Line regulation	V _{RLINE}	$8 V \le V_{IN} \le 40 V$	1,2,3		20	mV
	Load regulation	V _{RLOAD}	$0 \text{ mA} \leq I_L \leq 20 \text{ mA}$	1,2,3		50	mV
	Short-circuit current limit	los	V _{REF} = 0 V, T _A = +25° C	1		150	mA
	Temperature stability		2/	1,2,3		1	%
	Ripple rejection	ΔV _{IN} ΔV _{REF}	f = 120 Hz $T_A = +25^{\circ} \text{ C}$	4	60		dB
Oscillator section	Voltage stability	Δf _{OSC}	8 V ≤ V _{IN} ≤ 40 V T _A = +25° C	1		1	%
	Temperature stability		2/	1,2,3		6	%
Error amplifier	Input offset voltage	V _{IO}	V _{CM} = 2.5 V	1,2,3		5	mV
section	Input bias current	I _{IB}	V _{CM} = 2.5 V	1,2,3		10	μΑ
	Open loop gain	A _{VS}		<u>4,6</u> 5	72 66		dB
	Common mode rejection ratio	CMRR	1.8 V ≤ V _{CM} ≤ 3.4 V	4 5,6 <u>2</u> /	70		dB
	Output high level	V _{OH}	T _A = +25°C	1	3.8		V
	Output low level	V _{OL}	T _A = +25°C	1		0.5	V

See footnotes at end of table.

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TABLE I. <u>Electrical performance characteristics</u> - Continued.

Test		Symbol	Conditions 1/	Group A	Limits		Unit
			-55°C ≤ T _A ≤ +125°C unless otherwise specified	subgroups	Min	Max	
Comparator section	A min duty cycle	t _{ON} <u>4</u> /	V _{CM} = 2.5 V	4,5,6		0	%
	B min duty cycle	tosc (MIN)	V _{COMP} = 0.5 V				
	A max duty cycle	t _{ON} <u>4</u> /	V _{CM} = 2.5 V	4,5,6	45		%
	B max duty cycle	t _{OSC} (MAX)	V _{COMP} = 3.8 V				
Current limiting section	Sense voltage	V _{SEN}	Pin 9 = 2 V with error amplifier set for maximum out, T _A = +25° C	1	190	210	mV
	Common mode voltage	V _{CM}	<u>2</u> /	1,2,3	-0.3	+0.3	V
Output section	Emitter output voltage	V _{EO}	V _{IN} = 20 V dc	1,2,3	17		V
	Sat. voltage Side A 20 mA Side B 20 mA	V _{CE(SAT)}	I _C = 50 mA	1,2,3		2	V
	Collector leak- age current Side A Side B	I _{CEX}	V _{CE} = 40 V dc	1,2,3		50	μA
	Rise time A Rise time B	t _r	$R_C = 2 k\Omega$	9 10,11 <u>2</u> /		.4	μs
	Fall time A Fall time B	t _f	$R_C = 2 k\Omega$	9 10,11 <u>2</u> /		.2	μs
Standby curren	t	I _{IN}	V _{IN} = 40 V dc	1,2,3		10	mA

 $[\]underline{1}/\ V_{\mbox{\footnotesize{IN}}}$ = 20 V dc, f = 20 kHz, unless otherwise stated.

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^{2/} If not tested, shall be guaranteed.

<u>3</u>/ Guaranteed by correlation to other tested parameters.

 $[\]underline{4}/\ t_{\mbox{OSC}}$ is the period of the output waveform.

	8	9	COMPENSATION	
	8	9	COMPENSATION	
' '				
Ст	7	10	SHUTDOWN	
	6	11	EMITTER A	
	5	12	COLLECTOR A	
OUTPUT 📛	3 4	14 13	EMITTER B COLLECTOR B	
INPUT \(\begin{array}{c} \cdot \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\	2	15	V _{IN}	
NONTHIVE DITING	1	16	V _{REF}	

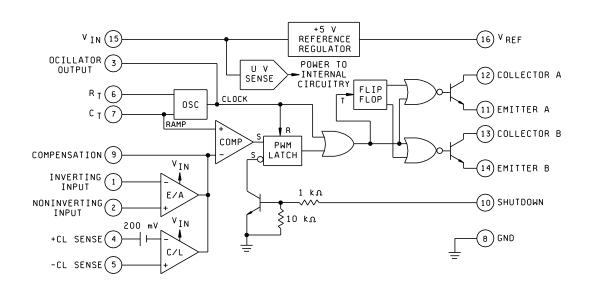


FIGURE 2. Logic diagram.

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- 3.7 <u>Notification of change</u>. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).
- 3.8 <u>Verification and review</u>. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 <u>Quality conformance inspection</u>. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 7 and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	
Final electrical test parameters (method 5004)	1*,2,3,4
Group A test requirements (method 5005)	1,2,3,4,5,6,9, 10,11**
Groups C and D end-point electrical parameters (method 5005)	1

PDA applies to subgroup 1. Subgroups 10 and 11, if not tested, shall be guaranteed to the limits specified in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
 - 6.2 Replaceability. Replaceability is determined as follows:
 - Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
 - When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/12601BEX.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 <u>Approved sources of supply</u>. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor	Vendor	Replacement
	CAGE	similar part	military specification
	number	number <u>1</u> /	part number
7802801EX <u>2</u> /	64155 48726 U4637 34333 18324	SG1524J/883B UC1524J/883B IP1524J/883B SG1524J/883B SG1524C/BEA	M38510/12601BEX

^{1/ &}lt;u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

2/ Inactive for new design use QPL M38510 product.

Vendor CAGEnumber_	Vendor name and address
18324	Signetics Corp 4130 S. Market Court Sacramento, CA 95834
34333	Silicon General 11861 Western Avenue Garden Grove, CA 92641
48726	Unitrode 7 Continental Boulevard Merrimack, NH 03054
64155	Linear Technology Corporation 1630 McCarthy Boulevard Milpitas, CA 95035-7487
U4637	Seagate Microelectronics LTD 900 Disc Drive Scotts Vally, CA 95066

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